



# Transistor

## a TFHE-friendly stream cipher

Jules Baudrin, Sonia Belaïd, Nicolas Bon, Christina Boura, Anne Canteaut, Gaëtan Leurent, Pascal Paillier, Léo Perrin, Matthieu Rivain, Yann Rotella, and Samuel Tap

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<https://eprint.iacr.org/2025/282>

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Part 1

# FHE and transciphering

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# Fully Homomorphic Encryption

Client

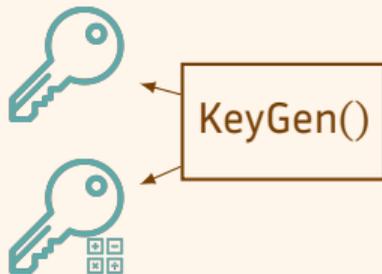


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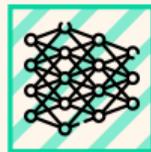


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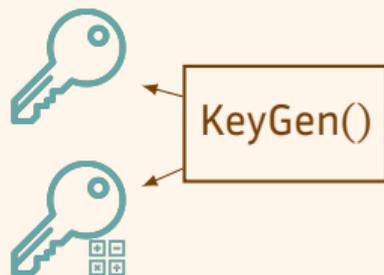


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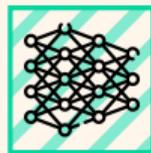


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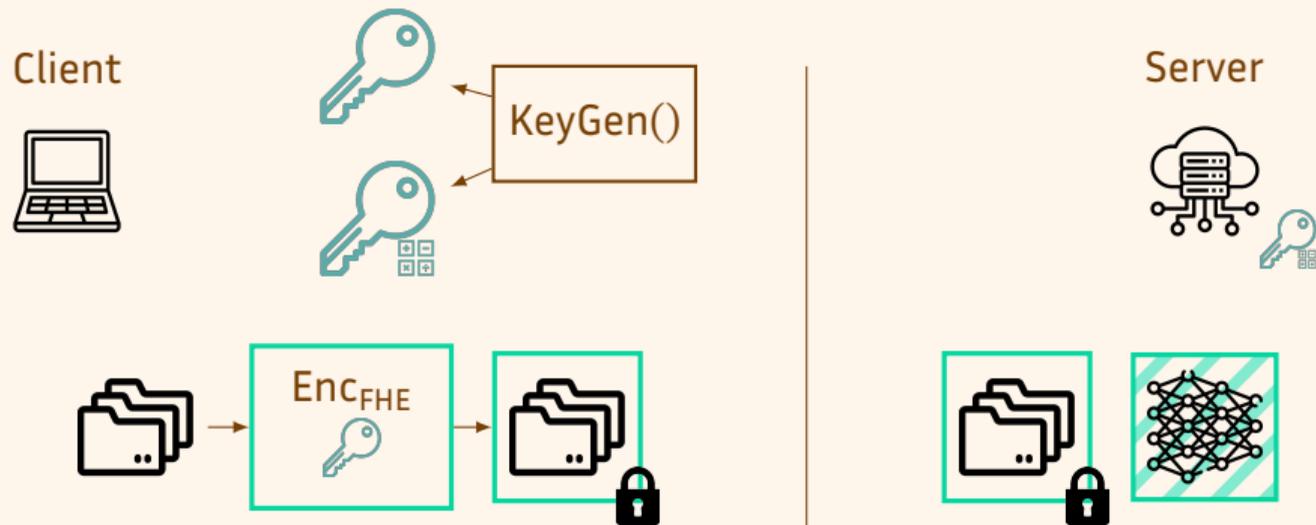
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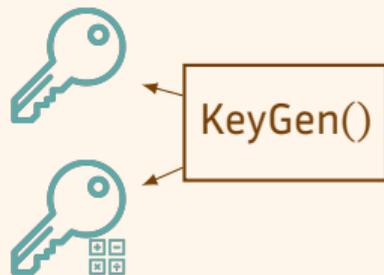


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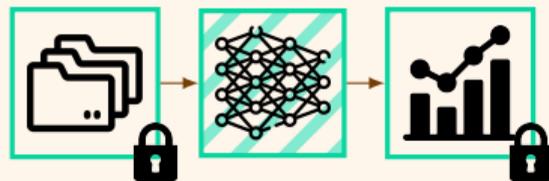


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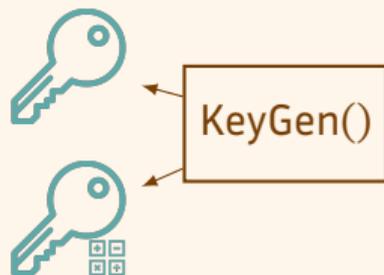


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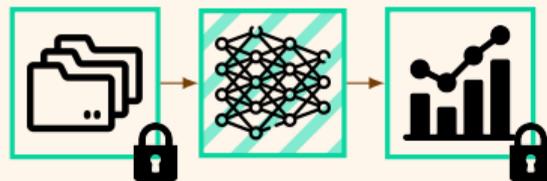


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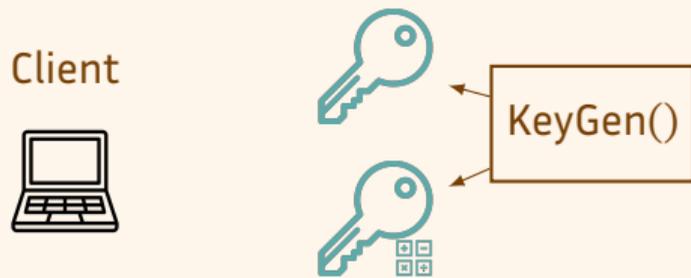
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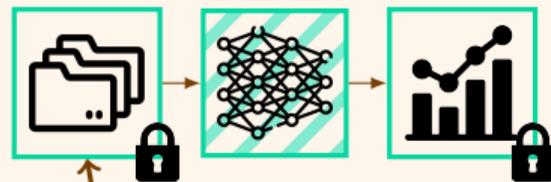
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# Fully Homomorphic Encryption



Server



# A solution: Transciphering

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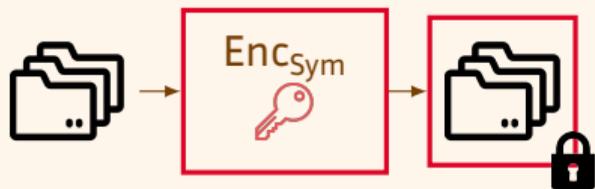


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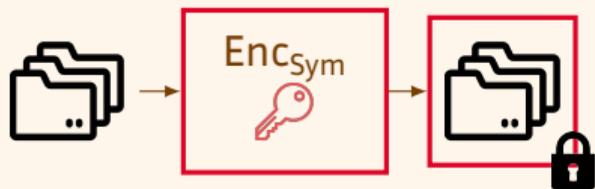


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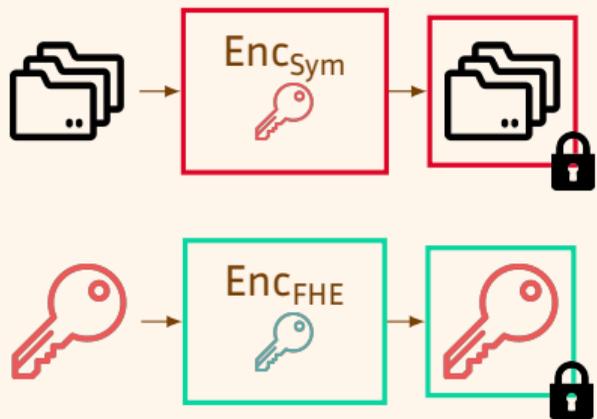


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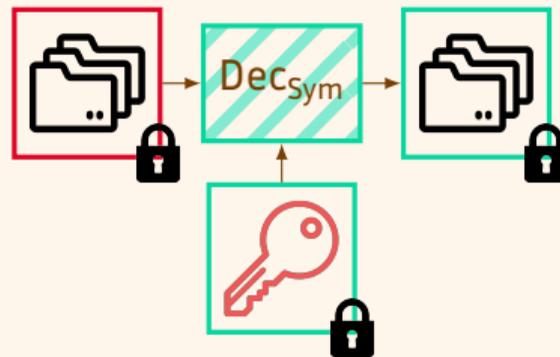


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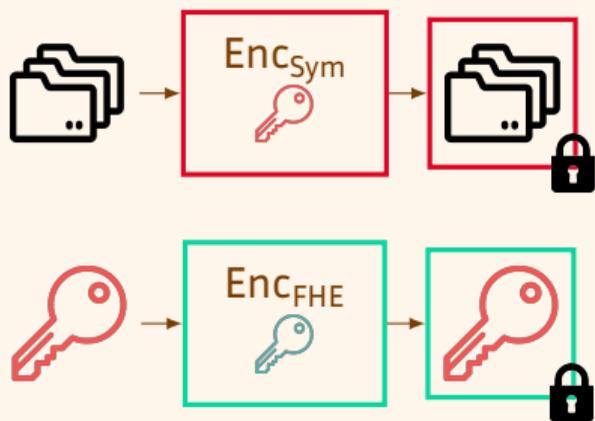


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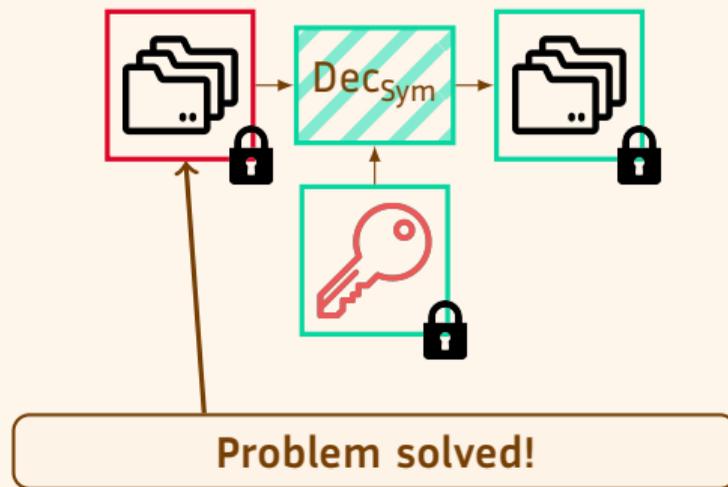


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# Which symmetric cipher ?

A standard one ?

- Evaluation of AES [GHS12] is too slow due to large Sbox size
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**With Transistor, we look for the best of both worlds (fast in FHE and secure)**

Part 2

TFHE and its specifies

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- Encryption:

$$\vec{c} = (a_0, \dots, a_{n-1}, b) \in \mathbb{Z}_q^{n+1}$$

with:

$$b = \langle \vec{a}, \vec{s} \rangle + \frac{q}{p} \cdot m + e.$$

$\vec{a}$  is random and  $e$  is a small Gaussian noise.

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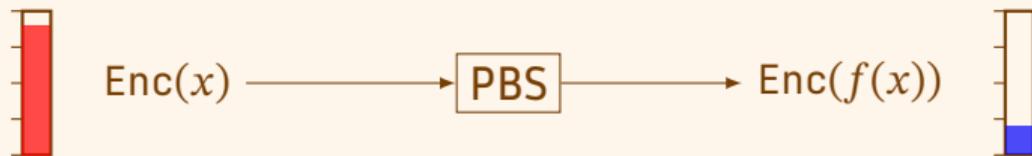
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- Programmable Bootstrapping (PBS): Very slow. Gets even slower as  $p$  increases.
  - **Resets** the noise to a nominal level
  - Evaluates a **Look-Up Table** from  $\mathbb{Z}_p$  to  $\mathbb{Z}_p$ .

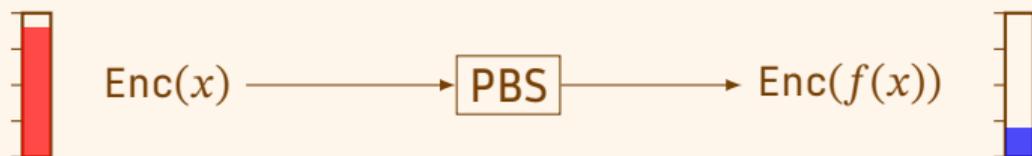
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- **Negacyclicity problem:**
  - If  $p$  is **even**, restricts the functions that can be evaluated.
  - Disappears when  $p$  is **odd**

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to guarantee the correctness of the computations.

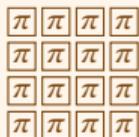
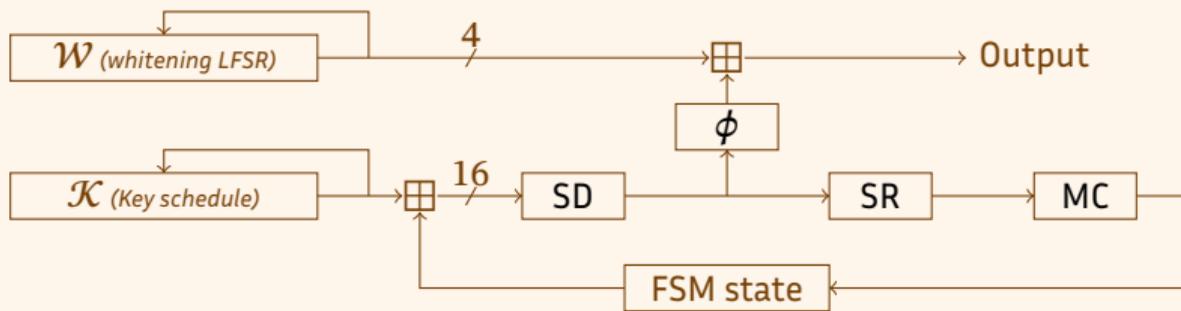
Part 3

# Description of Transistor

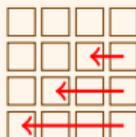
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# Design of Transistor

Prime field:  $\mathbb{F}_{17}$



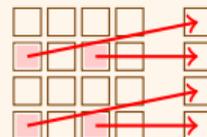
(a) SD.



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(c) MC.



(d)  $\phi$ .

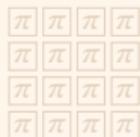
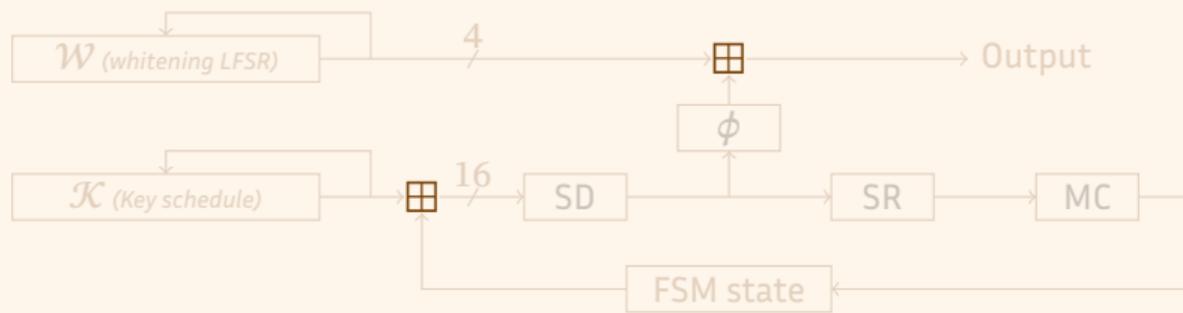
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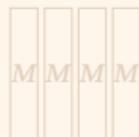
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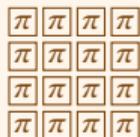
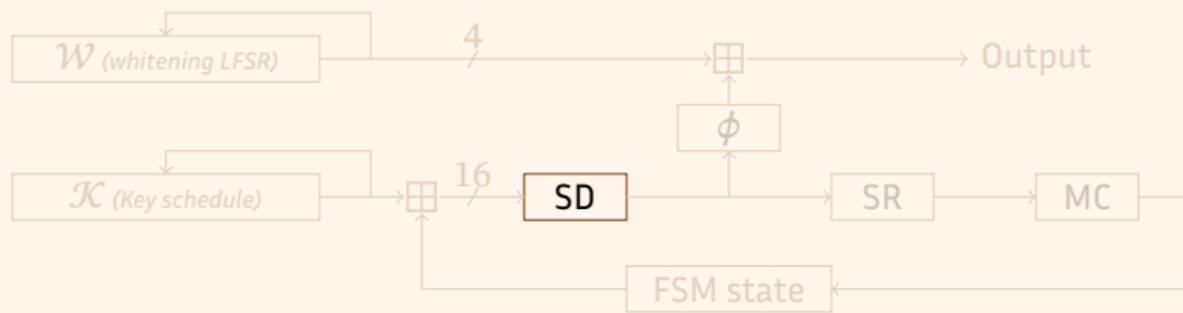
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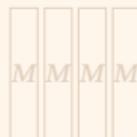
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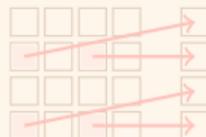
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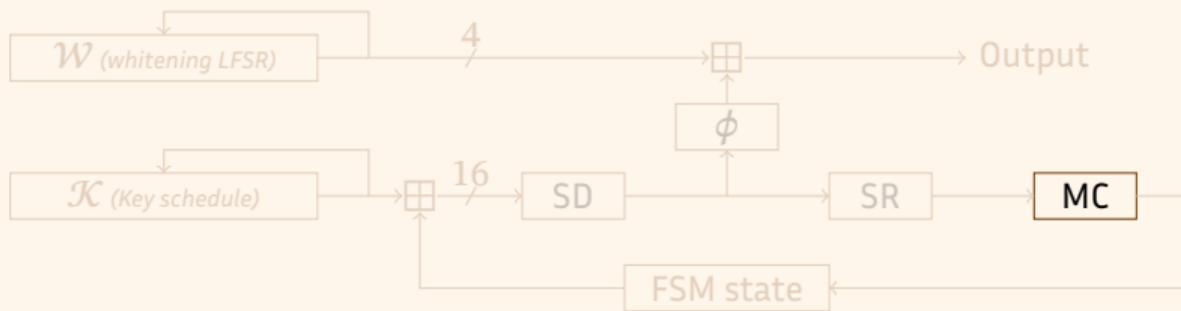
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# MixColumns

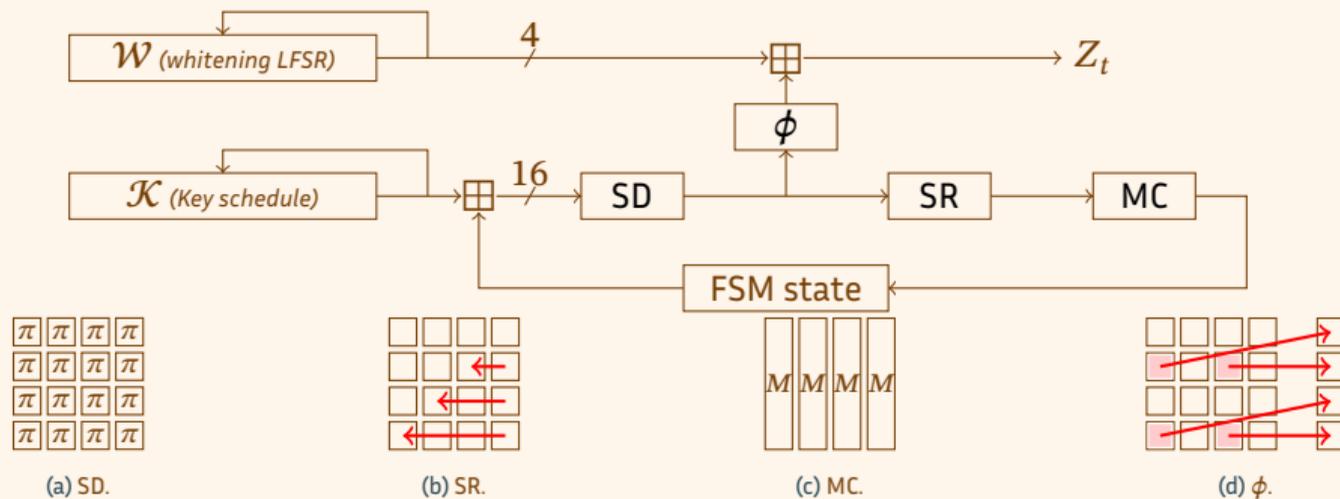
The matrix we chose for MixColumns is:

$$M = \begin{bmatrix} 2 & 1 & 1 & 1 \\ 1 & -1 & 1 & -2 \\ 1 & 1 & -2 & -1 \\ 1 & -2 & -1 & 1 \end{bmatrix}.$$

- Matrix MDS to ensure optimal diffusion,
- Symmetric,
- Minimal  $\ell_2$ -norm of 7  $\rightarrow$  important for noise management.

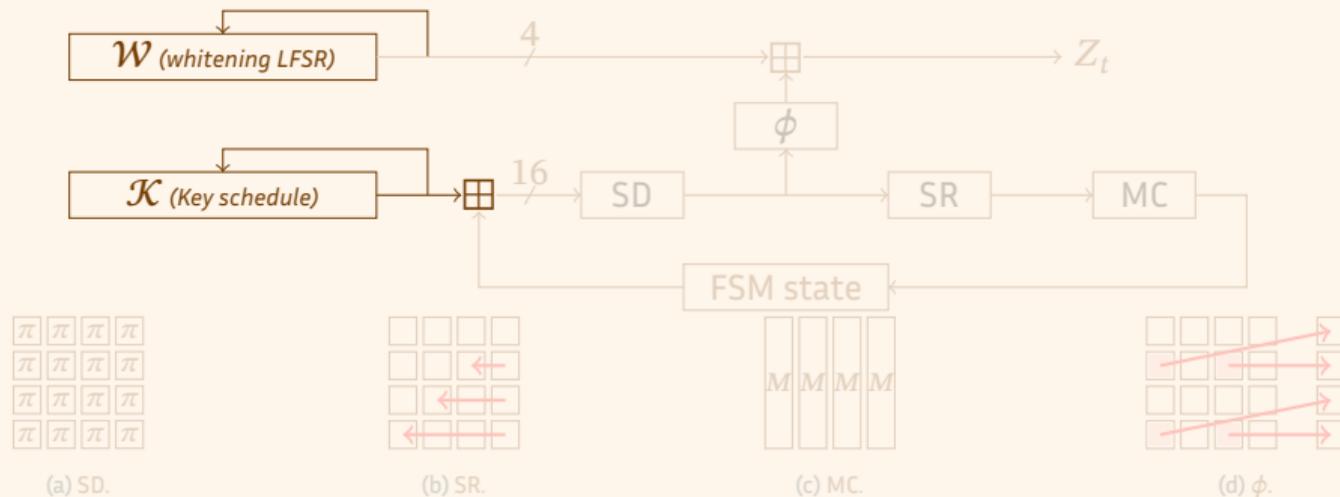
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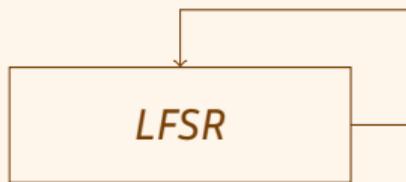


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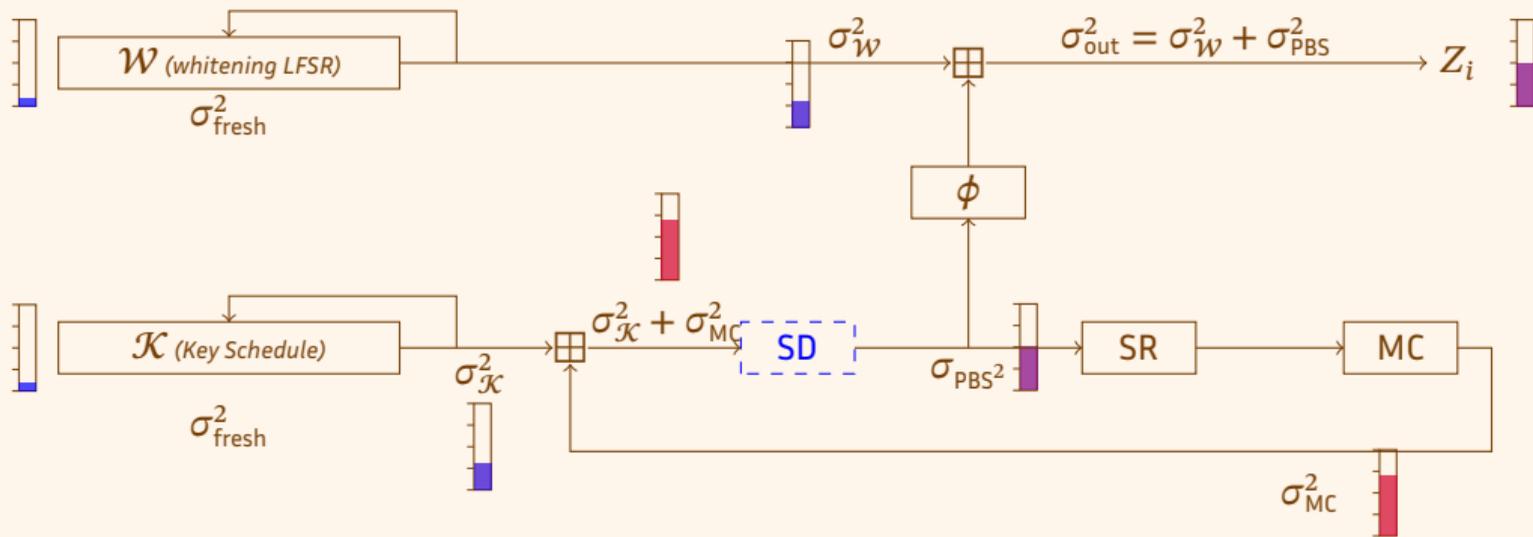
**The noise variance in the output of the silent LFSR remains stable over time, without using any PBS.**

Part 4

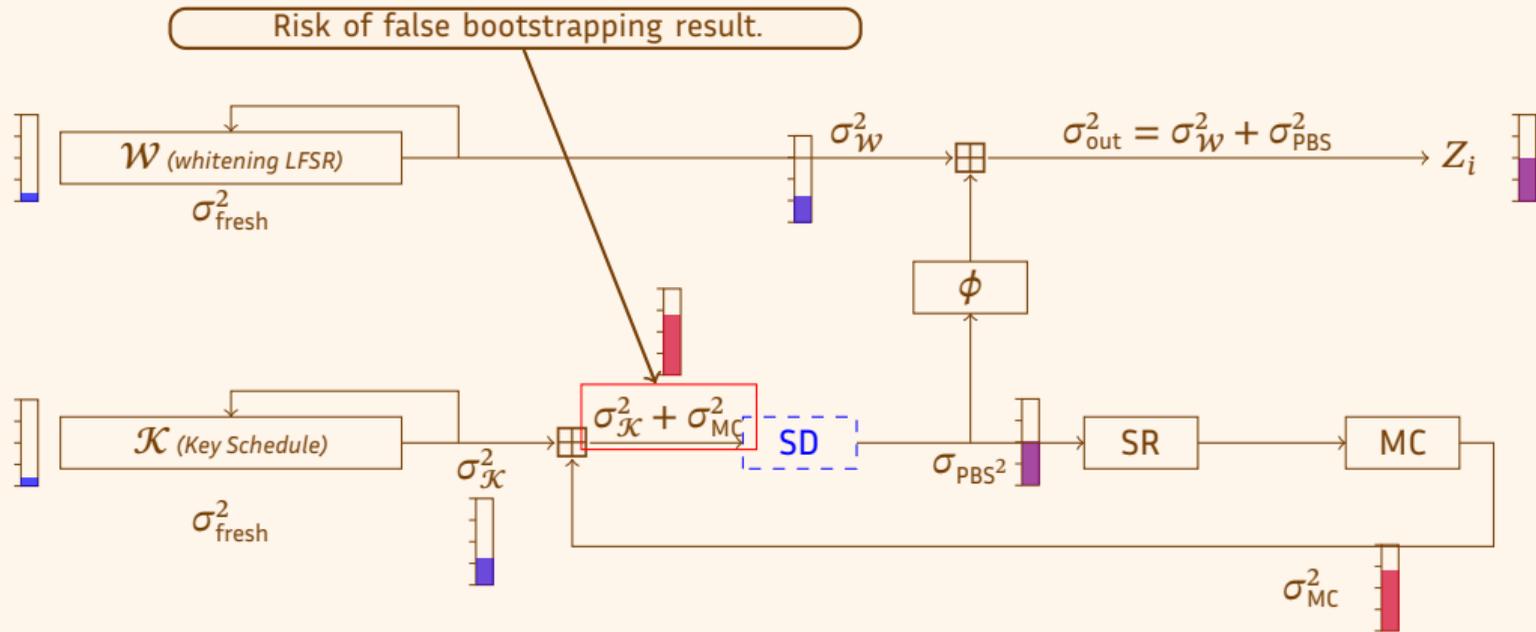
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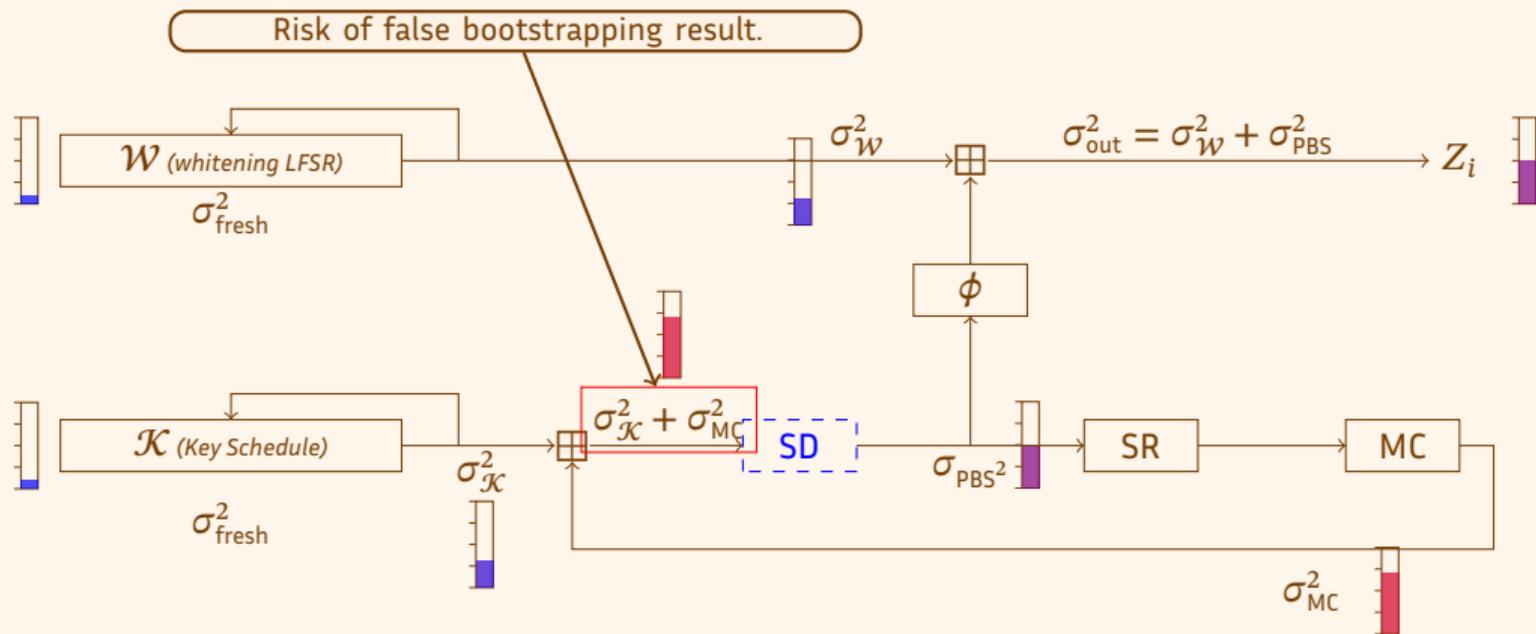
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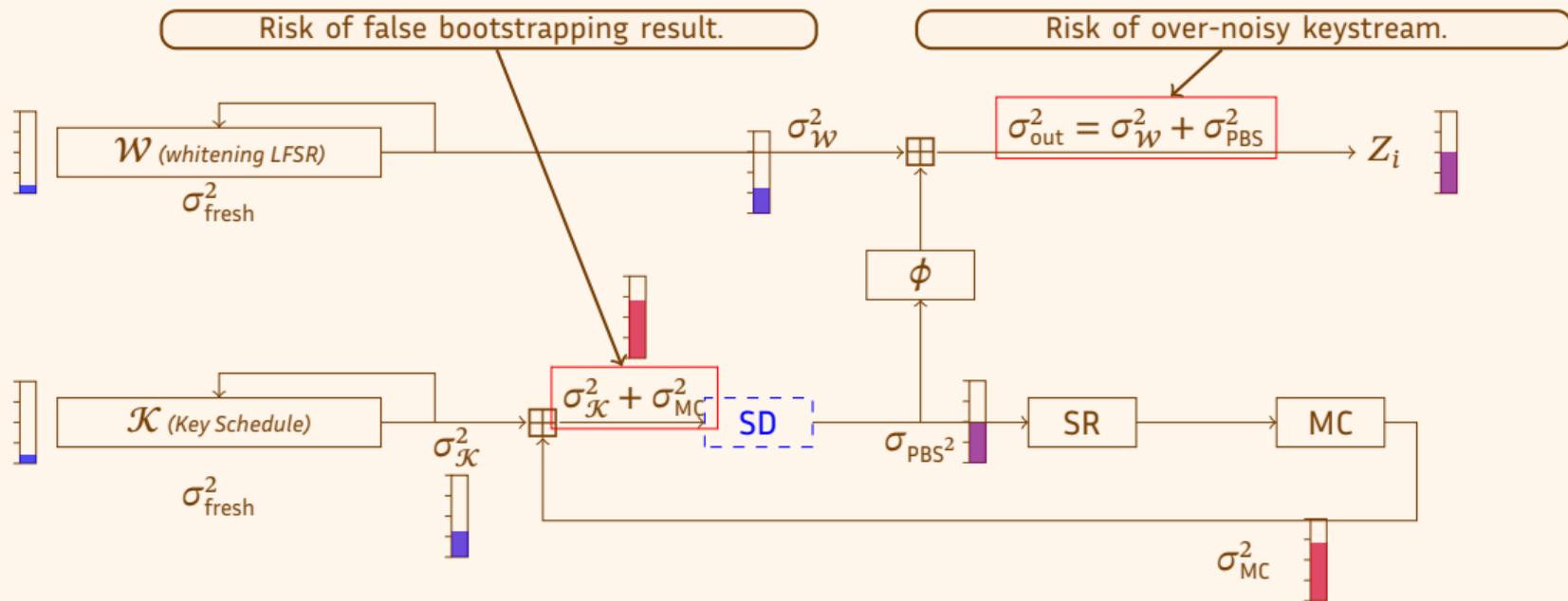


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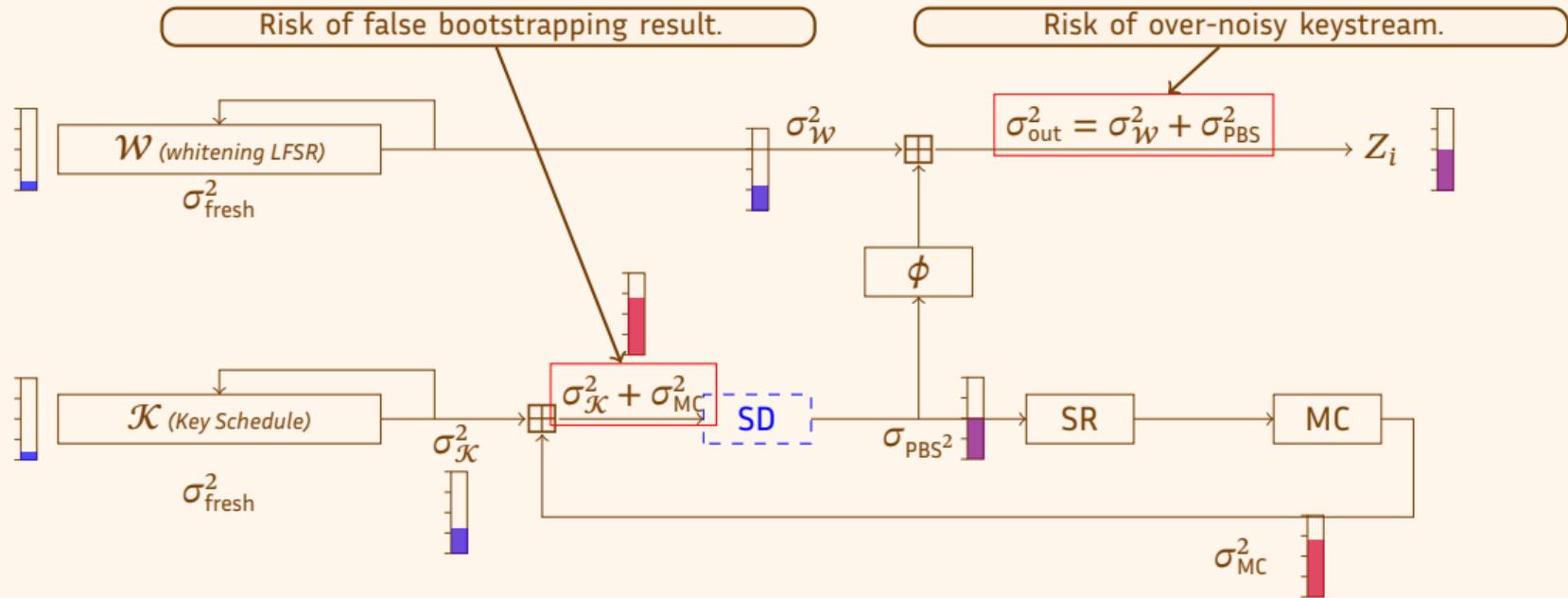
**Takeaway 1: No Restriction on the size of the LFSRs** (since  $\sigma_{\text{MC}}^2 \gg \sigma_{\mathcal{K}}^2$  and  $\sigma_{\text{PBS}}^2 \gg \sigma_{\mathcal{W}}^2$ )

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**Takeaway 2: Dimensioning the TFHE parameters can be reduced to select parameters for a simple PBS**

Part 5

# Cryptanalysis

---

# TMDTO and Guess & Determine

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Time-Memory Data Trade-Offs: Dimensions of the LFSRs:  $|\mathcal{K}| = 64$  and  $|\mathcal{W}| = 32$  elements of  $\mathbb{F}_{17}$ . Ensures a limit on the keystream of  $2^{31}$  digits with a single key.

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**Guess-and-Determine:** The filtering procedure of Transistor shows that the attacker has to guess the content of the whitening LFSR and  $\frac{12}{16} |\mathcal{W}|$  digits, leading to a complexity:

$$p^{\frac{12}{16}|\mathcal{K}|+|\mathcal{W}|} \approx 2^{294}.$$

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Main arguments of the proof:

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**Any correlation attack based on the span of a linear trail requires  $2^{41.5}$  digits of the output sequence.**

# And more!

More analysis in the paper about:

- Linear Distinguishers on the keystream,
- Algebraic attacks.

Part 6

# Performances

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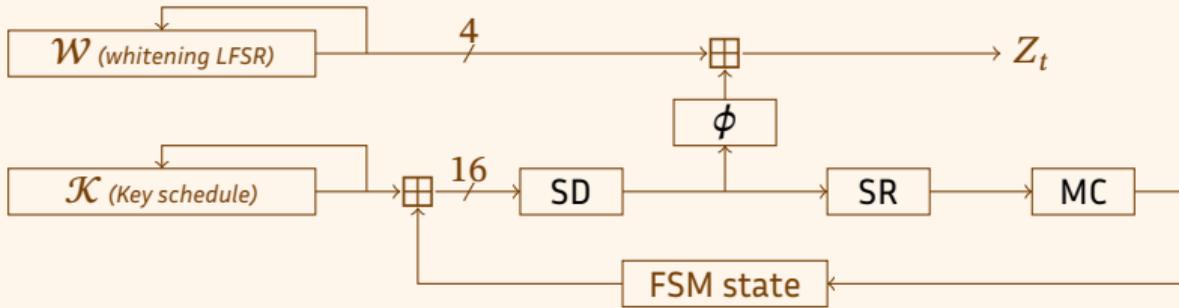
Cipher	Setup	Latency	Throughput	Communication Cost <sup>a</sup>	$p_{\text{err}}$
Trivium [BOS23] (128 thr.)	2259 ms	121 ms	529 bits/s	640 B + 35.6 MB †	$2^{-40}$
Kreyvium [BOS23] (128 thr.)	2883 ms	150 ms	427 bits/s	1024 B + 35.6 MB †	$2^{-40}$
Margrethe [AGHM24]	No	27.2 ms	147.06 bits/s	64 MB *	$< 2^{-1000}$
	No	54.2 ms	73.8 bits/s	128 MB *	$< 2^{-1000}$
PRF-based construction [DJL <sup>+</sup> 24]	No	5.675 ms	881 bits/s	32.8 MB = 8.9 MB + 23.9 MB	$2^{-64}$
FRAST [CCH <sup>+</sup> 24]	25 s (8 thr.)	6.2 s	20.66 bits/s	34.05 MB = 148 KB + 33.91 MB	$2^{-80}$
Transistor	No	251 ms	65.10 bits/s	13.54 MB = 780 B + 12.78 MB	$2^{-128}$

<sup>a</sup> Includes size of encrypted symmetric key + size of evaluation keys. † Values recomputed from the data of the papers. For consistency's sake, we applied the classical technique of ciphertexts compression to estimate the communication cost.

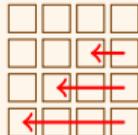
\* In Margrethe, no keyswitching nor bootstrapping keys are required.

# Thank You !

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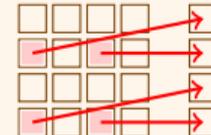
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